

Figure 1

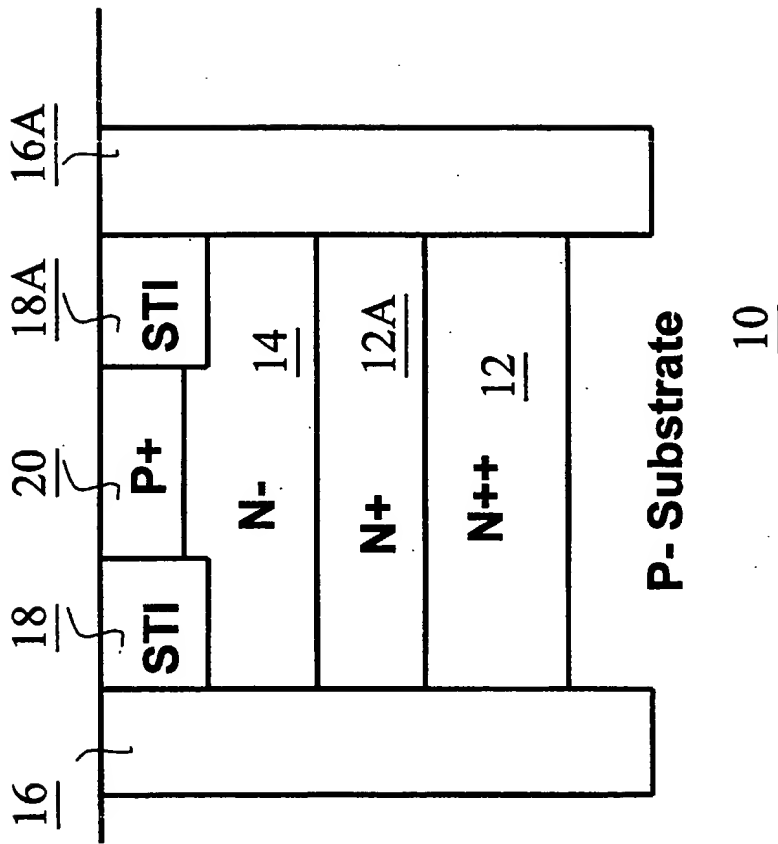


Figure 2

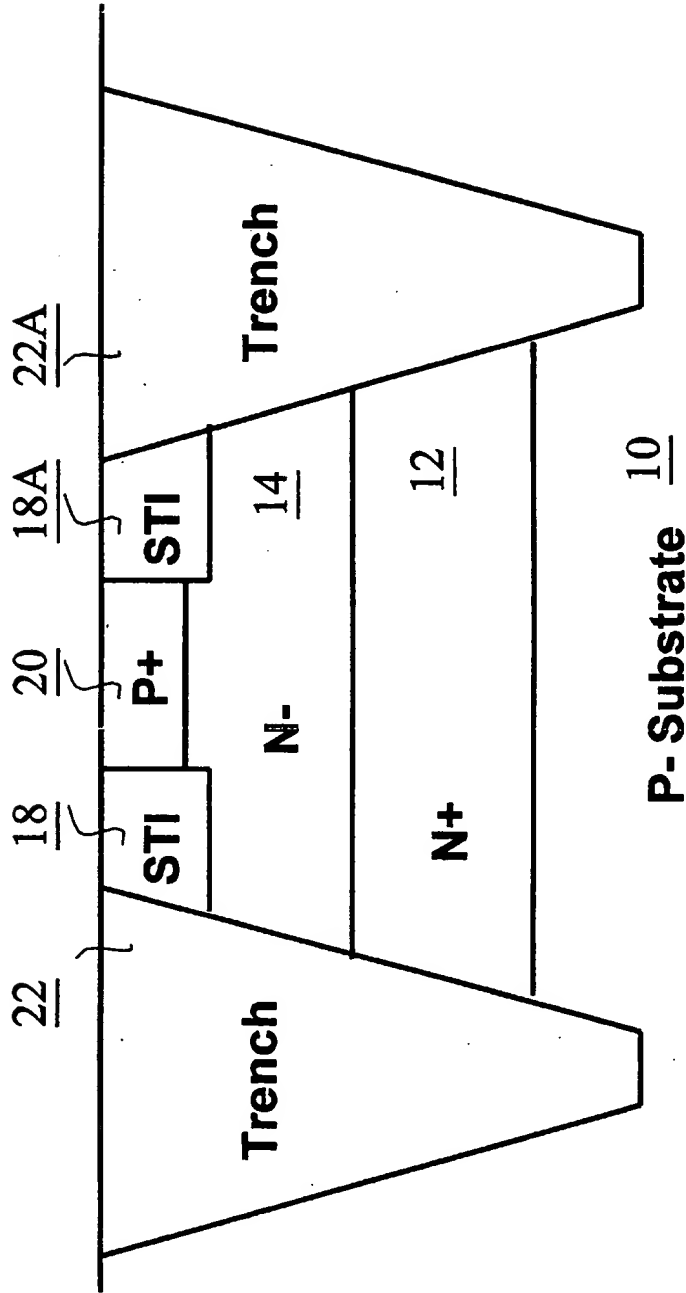
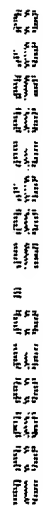
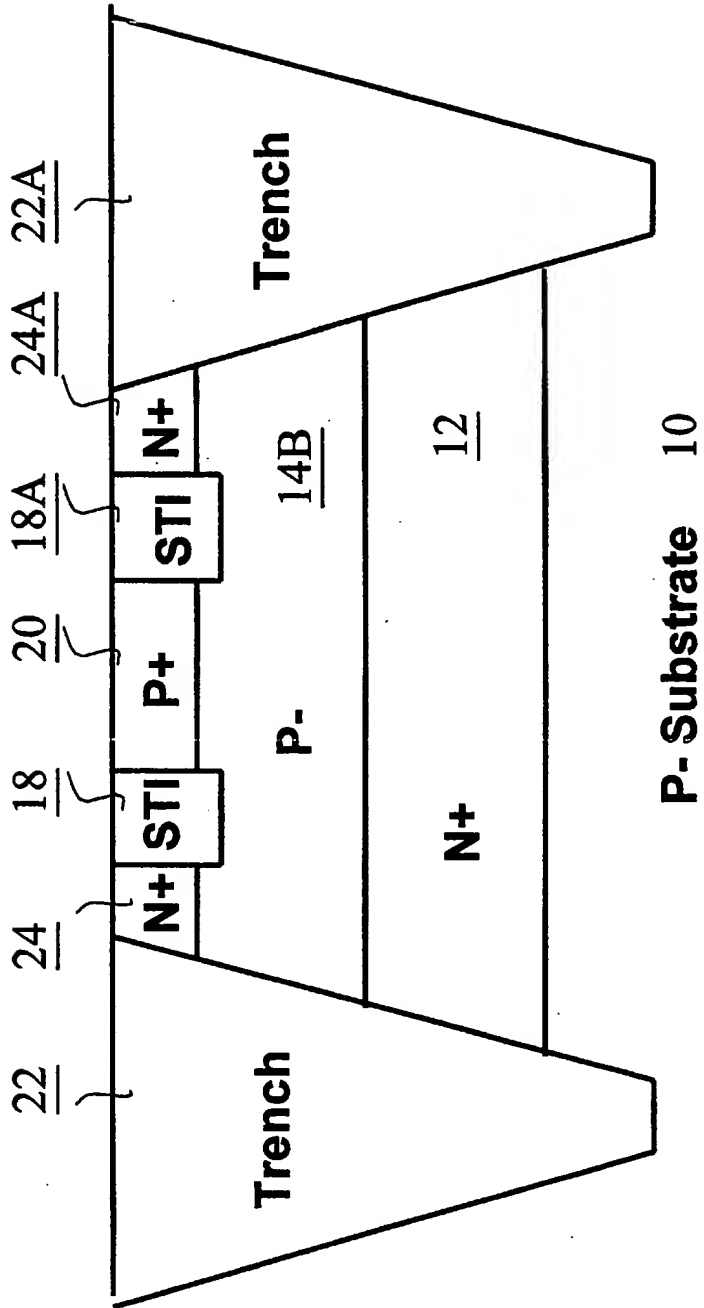


Figure 3



1



- Figure 5

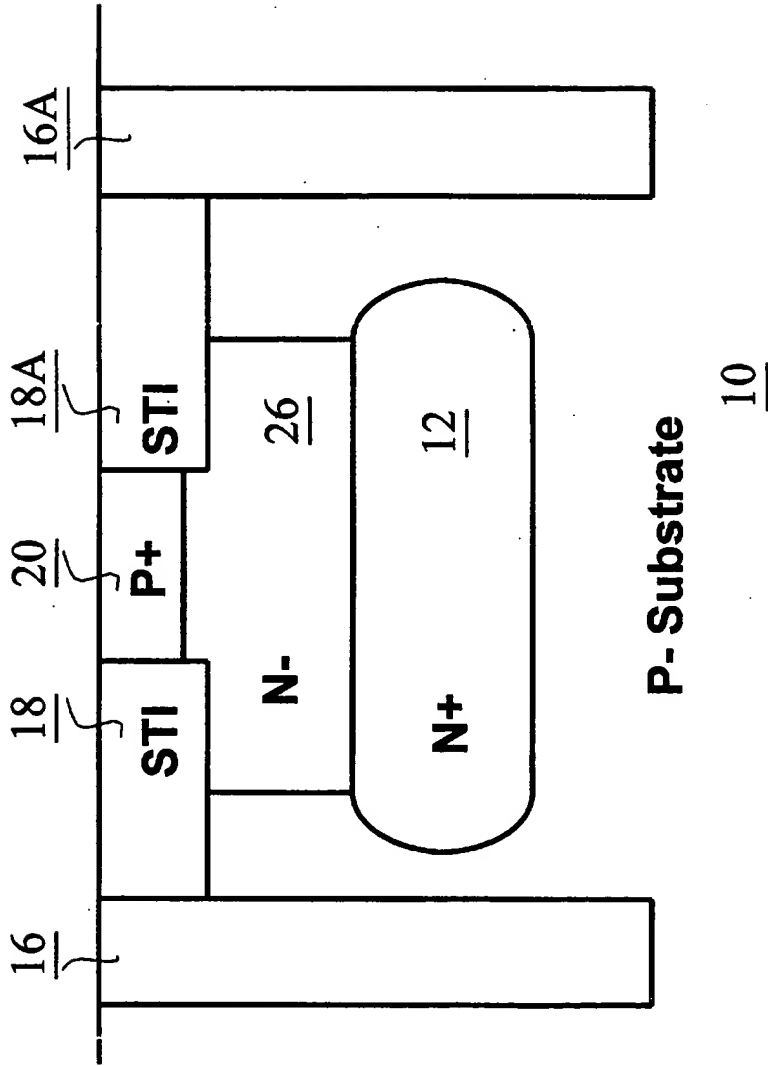


Figure 6

FIG. 7 is a cross-sectional view of a semiconductor device in accordance with the present invention.

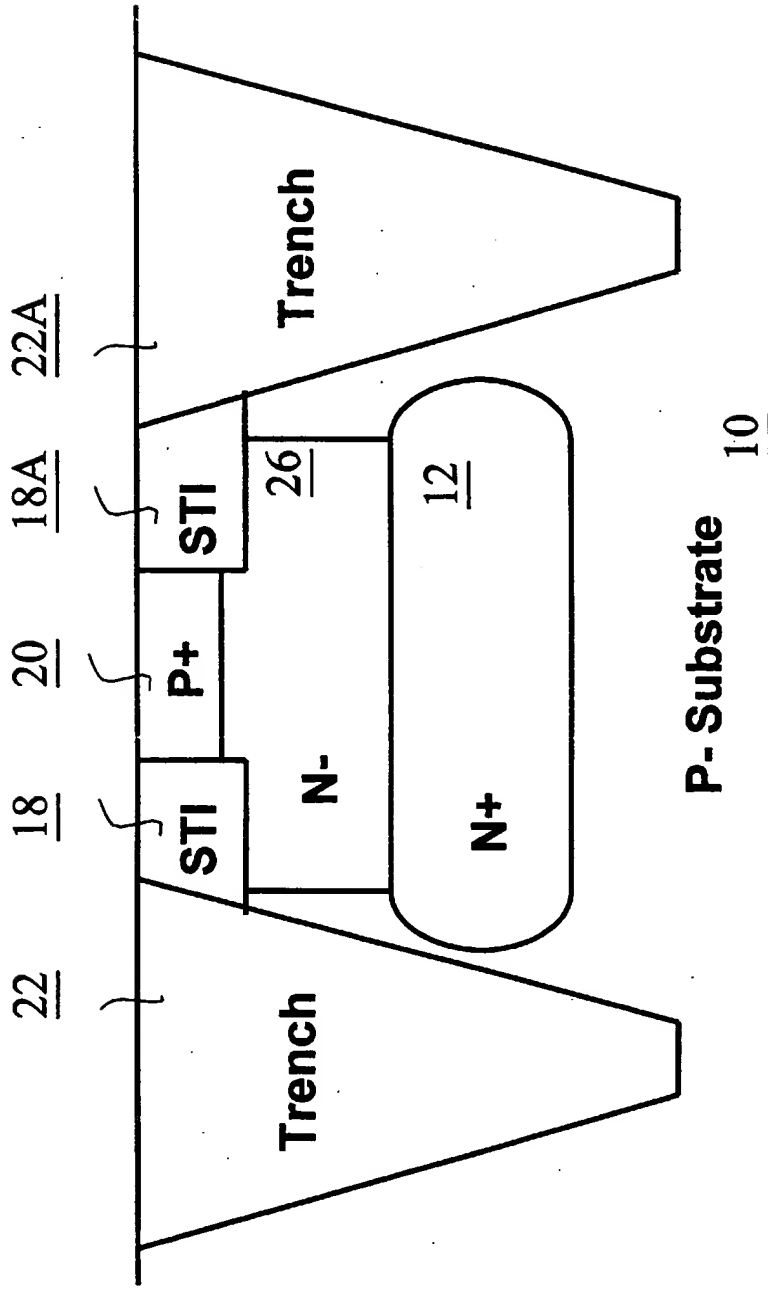


Figure 7